



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Shunichi SATO et al.

Serial No.: 10/646,438

Group Art Unit: 2814

Date Filed: August 22, 2003

Examiner: Wai Sing Louie

For: LIGHT EMITTING DEVICES WITH LAYERED III-V SEMICONDUCTOR

STRUCTURES AND MODULES AND SYSTEMS FOR COMPUTER, NETWORK

AND OPTICAL COMMUNICATION, USING SUCH DEVICES

I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Paul Teng Date Reg. No. 40,837 March 2, 2005

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO DECEMBER 9, 2004 OFFICE ACTION

Sir:

This response to the Office Action dated December 9, 2004 is submitted in connection with the above-identified application.

A Listing of Claims which begins on page 2 is provided for the convenience of the Examiner.

Remarks begin on page 14 of this paper.

A Terminal Disclaimer is attached hereto as Exhibit A.